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<u>L31</u>	118 and 122	28	<u>L31</u>
<u>L30</u>	118 and 121	65	<u>L30</u>
<u>L29</u>	118 and 120	124	<u>L29</u>
<u>L28</u>	118 and 126	218	<u>L28</u>
<u>L27</u>	L26 and 119	34	<u>L27</u>
<u>L26</u>	(712/2-300)[CCLS]	12719	<u>L26</u>
<u>L25</u>	119 and 122	4	<u>L25</u>
<u>L24</u>	119 and 121	11	<u>L24</u>
<u>L23</u>	119 and 120	21	<u>L23</u>
<u>L22</u>	(711/132)[CCLS]	145	<u>L22</u>
<u>L21</u>	(711/120-173) [CCLS]	19825	<u>L21</u>

<u>L20</u>	(712/200-219,225-227, 241-248)![CCLS]	5783	<u>L20</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L19</u>	L18 and (thread\$3 or multi near1 ththread\$3)	90	<u>L19</u>
<u>L18</u>	push\$4 near25 pop\$4 near25 stack near25 point\$3	549	<u>L18</u>
<u>L17</u>	push\$4 near25 pop\$4 near25 stack near25 (sram or static) near25 point\$3	0	<u>L17</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L16</u>	l3 and memor\$4	1	<u>L16</u>
<u>L15</u>	L14 and (push or pop)	29	<u>L15</u>
<u>L14</u>	stack near5 sram	109	<u>L14</u>
<u>L13</u>	L12 and sram	0	<u>L13</u>
<u>L12</u>	5946487.pn.	1	<u>L12</u>
<u>L11</u>	L10 and sram	0	<u>L11</u>
<u>L10</u>	5968169.pn.	1	<u>L10</u>
<u>L9</u>	L8 and sram	0	<u>L9</u>
<u>L8</u>	3889243.pn.	1	<u>L8</u>
<u>L7</u>	L4 and ram\$1	0	<u>L7</u>
<u>L6</u>	L4 and sram\$1	0	<u>L6</u>
<u>L5</u>	L4 and sram	0	<u>L5</u>
<u>L4</u>	L3 and offset	1	<u>L4</u>
<u>L3</u>	5542070.pn.	1	<u>L3</u>
<u>L2</u>	associat\$3 near8 pop near8 push	54	<u>L2</u>
<u>L1</u>	assosicat\$3 near8 pop near8 push	0	<u>L1</u>

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» Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Abstracting stack to detect obfuscated calls in binaries**
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